

Fig. 1

FIG. 2 is a schematic diagram of a device 10.

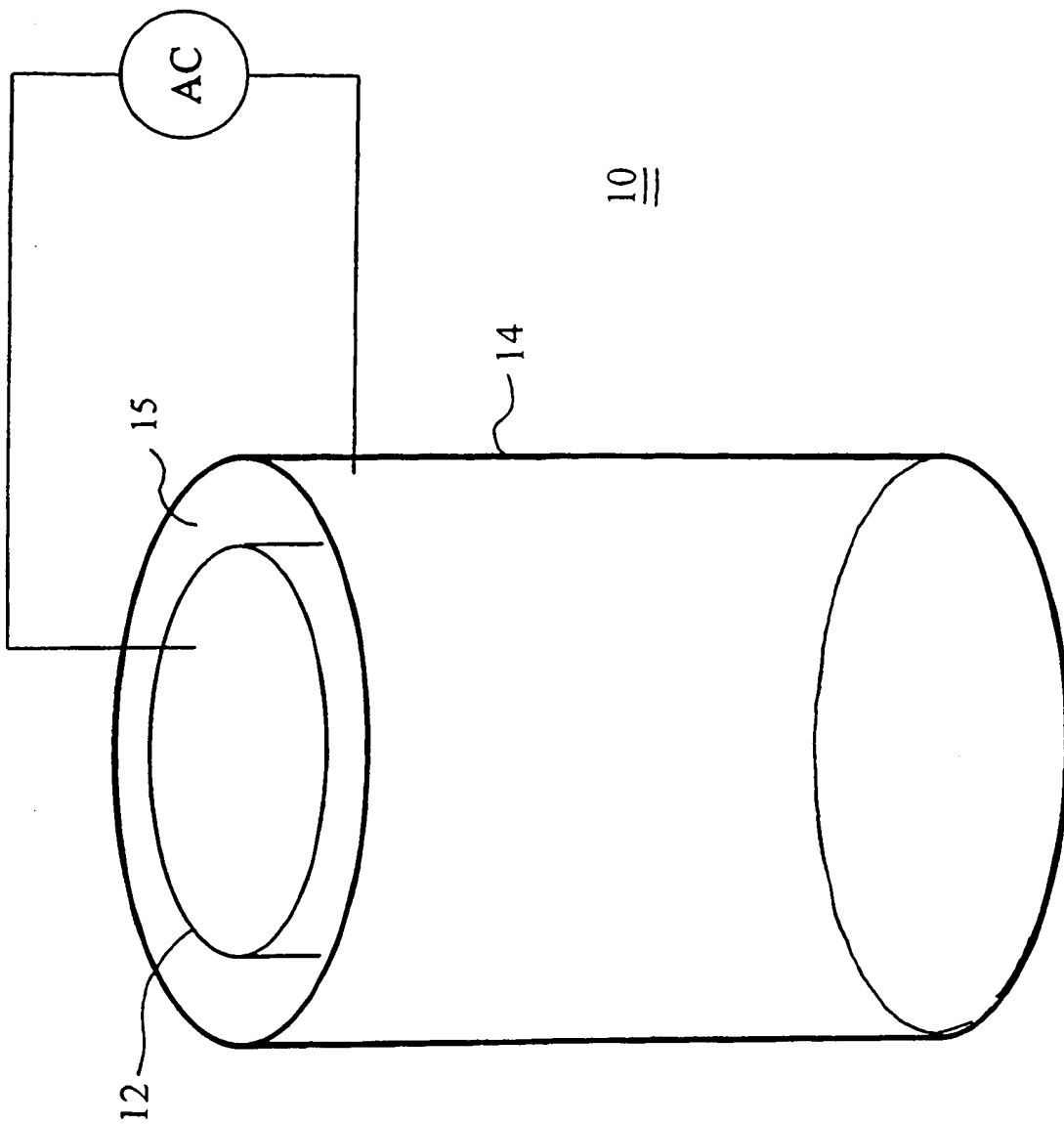


Fig. 2

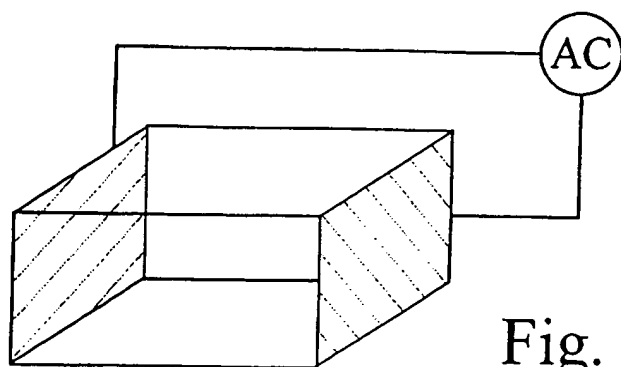


Fig. 3

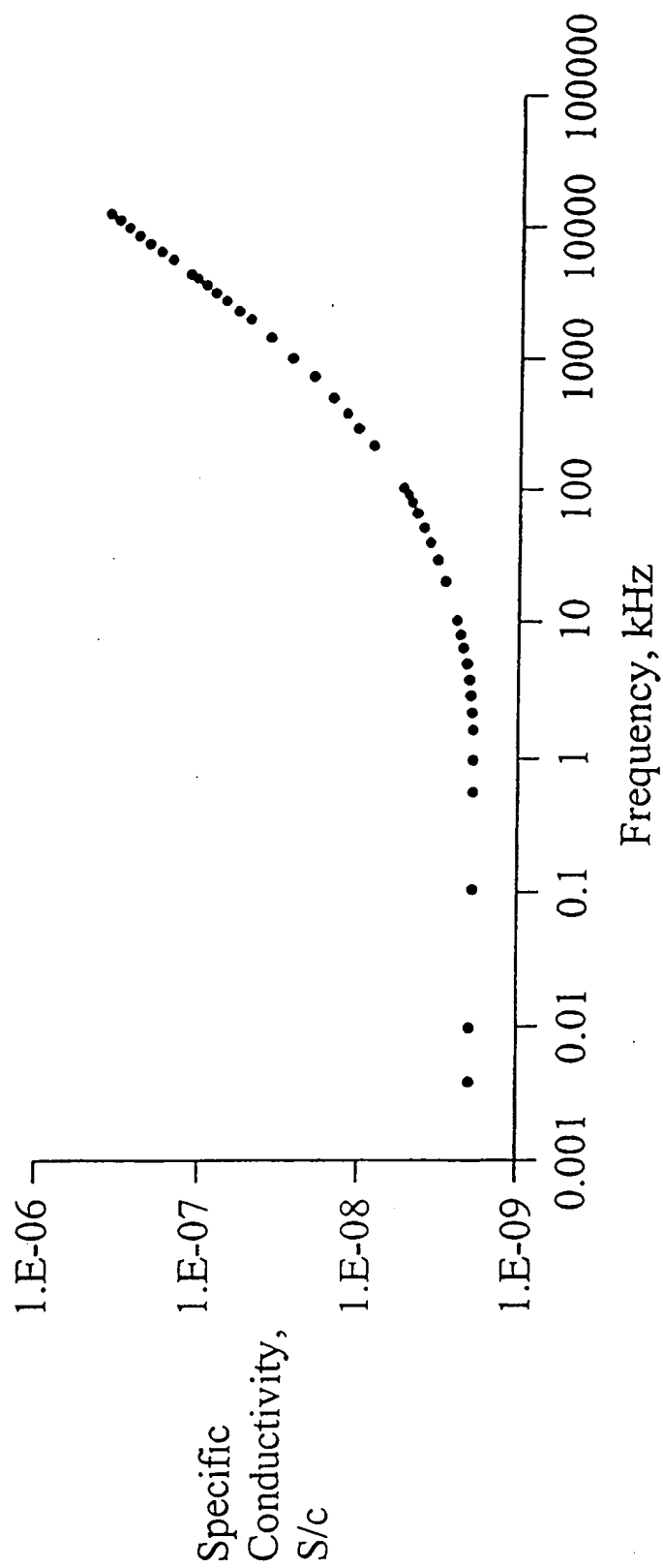


Fig. 4